## Application No. Applicant(s) IMACHINET AL

	10/001,030	I IMACOJI ET AL.	
Notice of Allowability	Examiner	Art Unit	•
	Livius R. Cazan	3729	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) of NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RICO of the Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in or other appropriate commu GHTS. This application is si	this application. If not included nication will be mailed in due course.	THIS initiative
1. $\boxtimes$ This communication is responsive to <u>the telephone interview</u>	v on 03/28/2006 and the fol	<u>'ow-up on 3/29/2006</u> .	
2. ⊠ The allowed claim(s) is/are <u>1-13</u> .			
3. Acknowledgment is made of a claim for foreign priority under a)   All b) □ Some* c) □ None of the:  1. ○ Certified copies of the priority documents have  2. □ Certified copies of the priority documents have  3. □ Copies of the certified copies of the priority documents have  3. □ Copies of the certified copies of the priority documents international Bureau (PCT Rule 17.2(a)).  * Certified copies not received: □	been received. been received in Application uments have been received of this communication to file ENT of this application.  tted. Note the attached EXAs reason(s) why the oath or	n No in this national stage application from a reply complying with the requireme MINER'S AMENDMENT or NOTICE	nts
(a) ☐ including changes required by the Notice of Draftsperso		( PTO-948) attached	
1)  hereto or 2) to Paper No./Mail Date		,	
(b) including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1.8 each sheet. Replacement sheet(s) should be labeled as such in the	B4(c)) should be written on the e header according to 37 CFI	e drawings in the front (not the back) c R 1.121(d).	of
<ol> <li>DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT F</li> </ol>	it of BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	RIAL must be submitted. Note the LOGICAL MATERIAL.	•
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5. ☐ Notice of Inf	ormal Patent Application (PTO-152)	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		mmary (PTO-413), Mail Date	
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date</li> </ol>		Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛭 Examiner's	Statement of Reasons for Allowance	
	9. ⊠ Other <u>Attacl</u>	nment.	
	SUPERI	PETER VO VISORY PATENT EXAMINER	

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## **EXAMINER'S AMENDMENT**

1. The amendments received on 3/13/2006 and 2/17/2006 have been fully

considered and made of record.

2. An examiner's amendment to the record appears below. Should the changes

and/or additions be unacceptable to applicant, an amendment may be filed as provided

by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be

submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given on 03/29/2006 in a

follow-up to a telephone interview with Harry J. Staas on 03/28/2006.

The application has been amended as follows:

Claim 1:

• In line 8, replace "material" with --metal material, different from the material of the

base,--

• In line 10, insert -- completely -- before "filling the"

• In line 10, replace "material" with --metal material different from said first metal

material,--

In line 12, replace "material" after "a third" with --metal material, different from

said second metal material,--

In line 20, insert --, thereby producing the wiring substrate -- after "of the bumps"

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For clarity, the claims (as amended by the examiner) have been included as an

attachment.

The following is an examiner's statement of reasons for allowance: the prior art 1. does not teach, in combination with all other claim limitations, a method for producing a wiring substrate provided with bumps protruding from a surface of the substrate, the method comprising the steps of: covering one side of a metallic base with an electrical insulating film and forming open holes in the insulating film so as to expose at the bottoms thereof the base, etching the base using the insulating film having the open holes formed as a mask to form concavities in the base, electroplating the interior face of each of the concavities with a first metal using the base as a plating power supply layer to form a barrier metal film on the interior face of each concavities, completely filling the concavities with a second metal material by electroplating, forming a barrier layer on the surface of the second metal by plating with a third metal, forming a stack of a predetermined number of wiring patterns on the insulating film, the wiring patterns being electrically connected to the second metal, removing the base from the stack of wiring patterns having bumps each having the barrier metal film, and removing the barrier metal film from each of the bumps. It should be noted that the admitted prior art together with the references cited by the examiner teach many the claim limitations individually (such as masking, etching to form concavities, electroplating, forming wiring patterns). However, no prior art references have been found which teach all of the steps

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in the particular order claimed by the applicants, and, taken together, the limitations of

claim 1 are not obvious over nor anticipated by the prior art.

Any comments considered necessary by applicant must be submitted no later

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than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Livius R. Cazan whose telephone number is (571) 272-

8032. The examiner can normally be reached on 7:30AM-4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor. Peter Vo can be reached on (571)272-4690. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

Status information for Patent Application Information Retrieval (PAIR) system.

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

LRC 03/29/2006

**PETER VO** SUPERVISORY PATENT EXAMINER

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## Attachment

Claim 1:

A method for producing a wiring substrate provided with bumps protruding from a surface of the substrate, the method comprising:

covering one side of a metallic base with an electrical insulating film and forming open holes in the insulating film so as to expose, at the bottoms thereof, the base;

etching the base using the insulating film, having the open holes formed therein as a mask, to form concavities in the base;

electroplating the interior face of each of the concavities, using the base as a plating power supply layer, with a first material metal material, different from the material of the base, to form a barrier metal film on the interior face of each of the concavities;

completely filling the concavities with a second, bump material metal material different from said first metal material, by electroplating, using the base as a plating power supply layer;

forming a barrier layer of a third material metal material, different from said second metal material, on the surface of the bump material in each concavity, using the base as a plating power supply layer;

forming a stack of a predetermined number of wiring patterns on the insulating film, the adjacent wiring patterns in the stack being separated from each other by an intervening insulating layer and being electrically connected to each other through vias formed in the intervening insulating layer and to the bump material in the concavities; and

removing the base from the stack of wiring patterns having bumps, each bump having the barrier metal film thereon, and removing the barrier metal film from each of the bumps, thereby producing the wiring substrate.

Claims 2-13 are identical to claims 2-13 of the amendment received on 3/13/2006.